# Design of a Pipelined Architecture for Ray/Bézier Patch Intersection Computation

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## Abstract

We describe an algorithm for computing ray/Bézier patch intersections from a pipelined hardware point-of-view. This algorithm uses patch subdivision and other geometrical techniques to find a given maximum number of intersection points nearest to the ray origin. We propose a pipeline-based hardware architecture, and verify the number of pipeline stages required by simulation.

Keywords and Phrases: pipelining, architecture, ray tracing, Bézier surfaces ACM Computing Classification categories: C.1, C.3, I.3.1, I.3.5

## **1** Introduction

Ray tracing is one of the most important techniques in computer graphics for rendering high quality and photorealistic pictures. In it, at least one ray is cast into the scene for each pixel of the image. If the ray hits an object, it may produce other rays, such as reflection, shadow, and refraction rays to determine the color of the associated pixel (see [1] for more details). Ray tracing deals with thousands, if not millions, of such ray-object intersection calculations in each image and is thus well known for its exorbitant computation time.

*Xpatch* is a program that uses this graphical technique to model an incident radar signal and generate the resulting radar crosssections of various objects. The object surfaces are modeled by different shape primitives such as meshes and parametric patches. Each type of primitive has its own algorithm to determine intersection with a given ray. Much work has been conducted at the Air Force Research Lab (AFRL) to determine which part of *Xpatch* is the most time consuming, finding that the majority of the time (between 50% and 80%) is spent in executing these algorithms.

Among commonly-used modelling primitives, Bézier patches stand out for being able to provide high accuracy of curved surfaces. Nevertheless, ray/Bézier patch intersection algorithms are computationally expensive. Many attempts have been made to improve them. The Bézier clipping algorithm [9] introduces an iterative geometric algorithm that finds all solutions of the ray-patch intersection problem up to an user definable accuracy, but it is hard to determine the number of iterative steps needed for a given patch. The Chebyshev boxing algorithm [5] replaces an original patch by many bilinear approximating patches, which will be subdivided into pieces repeatedly until the original patch has been well approximated. The preprocessing procedure required by this algorithm uses a recursive method to subdivide a given patch. The bounding volume hierarchy algorithm [3] combines the previous two by first carrying out the preprocessing procedure in the Chebyshev boxing algorithm and then calculating the tight bounding volumes.

In [10], one of us demonstrated that the depth of recursive calls using the Bézier clipping algorithm may vary greatly from patch to patch and from ray to ray. One patch may need a depth of three calls while another a depth of eleven, even though both resulted in a single intersection. All the above algorithms behave similarly, being based on recursive methods. Even if we were to implement the recursion by redundant hardware, they would still not be suitable for hardware acceleration because of the unpredictable resource allocation required.

Significant efforts to acclerate intersection computation have also been made on the hardware side. As reported by Arvo and Kirk in 1989 [1], these include:

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- LINKS-1, a 64-node (Intel 8086/8087) multiprocessor system that can be configured as a set of parallel pipelines to render a sequence of images.
- Kobayashi's work distributing the world database among a set of intersection processors.
- Goldsmith's work on a general-purpose multiprocessor system for ray-tracing on a hypercube.

More recent efforts include:

- Bouatouch's [2] exploitation of cache and virtual memory techniques to reduce message passing, yielding a speedup of about 60 on a 64-node MIMD.
- Kin and Kyung's [8] ring-structured multiprocessors, allowing a linear speedup by partitioning objects among processors.

All the aforementioned hardware solutions are based on general-purpose architectures that are suitable for executing any kind of ray/object intersection problems.

Earlier research conducted by one of us at AFRL in Summer 1998 [6] optimized a ray/triangular facet intersection algorithm for hardware realization and also proposed the corresponding hardware architecture. This article may therefore be considered an extension of that work, focussing on a similar approach for accelerating ray/Bézier patch intersection. As before, efforts have been made to:

- 1. Develop an algorithm suitable for hardware/software acceleration.
- 2. Define the algorithm-specific system architecture for physical implementation in the future.

We therefore present a new algorithm based on a set of subtasks including patch subdivision and related geometric techniques to find a given maximum number of intersection points that are nearest to the ray origin. We also propose a pipelined system architecture based on this algorithm. The number of coarse-grained pipeline stages was verified by extensive simulation.

Section 2 presents the proposed ray/Bézier patch intersection algorithm. Section 3 describes our test implementation for the algorithm. Section 4 describes the resulting architecture. Section 5 concludes the paper.



Figure 1: A Ray/Patch Intersection

## 2 The Ray/Bézier Patch Intersection Algorithm

In this Section, we will develop the mathematics of the patch intersection algorithm.

### 2.1 Background and Overview

Our study has been focused on a bicubic, nonrational, parametric Bézier patch, which we for simplicity will refer to hereafter as a "patch".

A ray, defined by an origin **O** and a direction **D**, intersects a patch as shown in Figure 1. There are a maximum of 18 possible intersection points when a ray pierces a patch [7], but from a ray tracing standpoint, we are only interested in finding the intersection closest to **O**. We therefore arbitrarily limit our interest here to successively finding and then refining (at most)  $N_i$  intersection points that are nearest to **O** and then selecting the closest of these as the answer.

One of the major difficulties of the ray/patch intersection problem is that exact solutions to the problem require finding roots of very high-degree polynomials, which cannot in general be done analytically and whose numerical solution is error-prone. Hence, we must look for approximate solutions.

Figure 2 shows how a mesh of 16 control points define a patch. The convex hull property states that the patch must lie inside its convex hull: the smallest (intuitively, "shrink-wrapped") polyhedron containing all the control points [11]. It is therefore a necessary condition for a ray to intersect a patch that the ray must intersect the patch's convex hull. It is not, however, a sufficient condition: a ray that passed through the convex hull may still miss the patch, but if we subdivide the patch into smaller patches and then consider the intersection of the ray with these smaller patches, missing the patch becomes less likely as the union of the convex hulls of the subpatches approaches the original patch. When a ray hits the convex hull of



Figure 2: The Control Mesh of a Bézier Patch

a sufficiently small patch, the center of the patch can be considered as a good approximation to the ray/patch intersection.

To find a convex hull intersection, then, we first consider the projection-based approach illustrated in Figure 3. Imagine that a ray is contained in the intersection of two perpendicular planes U and V, and that these, the patch, and its convex hull are all projected onto a third plane perpendicular to the ray direction. We use the U and V intersections to define a coordinate system whose origin is the projection of the ray.

In the projected 2D coordinate system, if the 16 projected control points are located on the same side of either one of the two axes, the ray must have missed the convex hull of the testing patch (Figures 3b and 3c). Otherwise the ray may hit the convex hull (Figure 3d).

Based on the above discussion, we outline the ray/patch intersection algorithm we will use:

- 1. Divide an input patch into  $4N_i$  subpatches. (If  $N_i$  is 4, this could be done with two four-way subdivision levels.)
- 2. Find all subpatches that are possibly intersected by the ray, based on the convex hull property.
- 3. If no subpatches pass the convex hull test, exit with a null intersection result.
- 4. From all possibly-intersecting subpatches, find the (at most)  $N_i$  subpatches that are closest to the origin of the ray.
- 5. If the subpatches are sufficiently small, exit with the center of the closest patch to the origin as the intersection.
- 6. Subdivide the  $N_i$  subpatches to obtain  $4N_i$  smaller subpatches.
- 7. Repeat steps 2 through 7.



Figure 4: Plane Generation

#### 2.2 Notation and Preliminaries

A ray **R** is represented in the form  $\mathbf{R} = \mathbf{O} + \mathbf{D}t$ . For our purposes, we will assume that **D** is normalized. A plane in Euclidean 3-space is defined (as usual) by the equation

$$Ax + By + Cz + E = 0 \tag{1}$$

Within a nonzero scaling factor, the normal to such a plane is the vector  $[ABC]^{t}$ . A nonrational parametric Bézier patch of degree m is defined by

$$\mathbf{Q}(u,v) = \sum_{i=0}^{n} \sum_{j=0}^{m} \mathbf{P}_{ij} B_{jm}(u) B_{in}(v)$$
(2)

where  $(u, v) \in [0, 1]$  are the parameters,  $\mathbf{P}_{ij}$  are the patch's control points, and the blending functions are the Bernstein polynomials:

$$B_{jm}(u) = \begin{pmatrix} m \\ j \end{pmatrix} u^{j} (1-u)^{m-j}$$
(3)

In matrix form, (2) becomes

$$\mathbf{Q}(u,v) = \mathbf{U}\mathbf{M}\mathbf{P}\mathbf{M}^{t}\mathbf{V}.$$
(4)

where  $\mathbf{P}$  is the control point matrix,  $\mathbf{M}$  is the Bézier basis matrix, and  $\mathbf{M}^t$  is the transpose of  $\mathbf{M}$ .

The bicubic patch is a special case of (4) with n = m = 3 where

$$\mathbf{M} = \begin{bmatrix} -1 & 3 & -3 & 1 \\ 3 & -6 & 3 & 0 \\ -3 & 3 & 0 & 0 \\ 1 & 0 & 0 & 0 \end{bmatrix}$$
$$\mathbf{P} = \begin{bmatrix} \mathbf{P}_{00} & \mathbf{P}_{01} & \mathbf{P}_{02} & \mathbf{P}_{03} \\ \mathbf{P}_{10} & \mathbf{P}_{11} & \mathbf{P}_{12} & \mathbf{P}_{13} \\ \mathbf{P}_{20} & \mathbf{P}_{21} & \mathbf{P}_{22} & \mathbf{P}_{23} \\ \mathbf{P}_{30} & \mathbf{P}_{31} & \mathbf{P}_{32} & \mathbf{P}_{33} \end{bmatrix}$$

#### 2.3 Plane Generation

and

Given a ray **R**, we need to find two mutually perpendicular planes U and V that contain the ray. If  $N_u$  and  $N_v$  are the



Figure 3: Testing Convex Hull Points. (a) This is the basic geometry. (b) The ray misses the patch vertically. (c) The ray misses the patch horizontally. (d) The ray may hit the convex hull of the patch. (In this case, it does.)

normals of the two planes, respectively, then  $N_u$ ,  $N_v$ , and D are all mutually perpendicular, as illustrated in Figure 4.

Of course, an infinite number of vectors are perpendicular to **D**. A simple and robust algorithm to choose two mutually perpendicular ones is:

- 1. let  $\xi$  be x, y, or z such that  $D_{\xi} = \min(|D_x|, |D_y|, |D_z|)$
- 2. let **T** be the unit vector in the  $\xi$  direction (i.e.,  $T_i = \delta_{i\xi}$ )
- 3.  $\mathbf{N}_u = \mathbf{T} \times \mathbf{D}$
- 4.  $\mathbf{N}_v = \mathbf{N}_u \times \mathbf{D}$

This could easily be implemented in hardware, especially since the first cross-multiplication is simply a rearrangement of the elements of  $\mathbf{D}$ .

#### 2.4 Patch Subdivision

The patch subdivision algorithm described here is based on the de Casteljau algorithm [5]. Control points of all the subpatches are generated by matrix multiplications which are easily and parallelizably performed in hardware. We decompose the algorithm into two steps. In the first step, we subdivide the range of the parameter u, deriving left  $(0 \le u \le \frac{1}{2})$  and right  $(\frac{1}{2} \le u \le 1)$  subpatch control vertices and respectively. The left subpatch  $\mathbf{Q}^L$  is defined by

$$\mathbf{Q}^{L}(u,v) = \mathbf{Q}\left(\frac{u}{2},v\right).$$
(5)

Using (4), we can rewrite this as

$$\mathbf{UMP}^{L}\mathbf{M}^{t}\mathbf{V}$$
(6)  
=  $\begin{bmatrix} \frac{u^{3}}{8} & \frac{u^{2}}{4} & \frac{u}{2} & 1 \end{bmatrix} \mathbf{MPM}^{t}\mathbf{V}$   
=  $\mathbf{U}\frac{1}{8}\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 2 & 0 & 0 \\ 0 & 0 & 4 & 0 \\ 0 & 0 & 0 & 8 \end{bmatrix} \mathbf{MPM}^{t}\mathbf{V}.$ 

Since this must be true for all U and V, we infer that

$$\mathbf{P}^{L} = \mathbf{M}^{-1} \frac{1}{8} \begin{bmatrix} 1 & 0 & 0 & 0\\ 0 & 2 & 0 & 0\\ 0 & 0 & 4 & 0\\ 0 & 0 & 0 & 8 \end{bmatrix} \mathbf{M} \mathbf{P} = \mathbf{D}^{L} \mathbf{P}$$
(7)

where

$$\mathbf{D}^{L} = \frac{1}{8} \begin{bmatrix} 8 & 0 & 0 & 0 \\ 4 & 4 & 0 & 0 \\ 2 & 4 & 2 & 0 \\ 1 & 3 & 3 & 1 \end{bmatrix}.$$

The right subpatch QR is defined by:

$$\mathbf{Q}^{R}(u,v) = \mathbf{Q}\left(\frac{u+1}{2},v\right).$$
(8)

Again using (4), we find that

$$\mathbf{P}^{R} = \mathbf{M}^{-1} \frac{1}{8} \begin{bmatrix} 1 & 0 & 0 & 0 \\ 3 & 2 & 0 & 0 \\ 3 & 4 & 4 & 0 \\ 1 & 2 & 4 & 8 \end{bmatrix} \mathbf{M} \mathbf{P} = \mathbf{D}^{R} \mathbf{P} \qquad (9)$$

where

$$\mathbf{D}^{R} = \frac{1}{8} \begin{bmatrix} 1 & 3 & 3 & 1 \\ 0 & 2 & 4 & 2 \\ 0 & 0 & 4 & 4 \\ 0 & 0 & 0 & 8 \end{bmatrix}.$$

In the second step, we apply the same method in the v direction, creating the top and bottom patch control vertices  $\mathbf{P}^{T}$  and  $\mathbf{P}^{B}$ . It is easy to show that these are related to a control vertex matrix  $\mathbf{P}'$  via  $\mathbf{P}^{T} = \mathbf{P}'\mathbf{D}^{T}$  and  $\mathbf{PB} = \mathbf{P}'\mathbf{D}^{B}$ , where

$$\mathbf{D}^{T} = \frac{1}{8} \begin{bmatrix} 8 & 4 & 2 & 1 \\ 0 & 4 & 4 & 3 \\ 0 & 0 & 2 & 3 \\ 0 & 0 & 0 & 1 \end{bmatrix} = \begin{bmatrix} \mathbf{D}^{L} \end{bmatrix}^{t}$$
(10)

and

$$\mathbf{D}^{B} = \frac{1}{8} \begin{bmatrix} 1 & 0 & 0 & 0 \\ 3 & 2 & 0 & 0 \\ 3 & 4 & 4 & 0 \\ 1 & 2 & 4 & 8 \end{bmatrix} = \begin{bmatrix} \mathbf{D}^{R} \end{bmatrix}^{t}.$$
 (11)

Combining the two steps, we derive the control vertex arrays of all four subpatches as

$$\begin{bmatrix} \mathbf{P}^{TL} & \mathbf{P}^{TR} \\ \mathbf{P}^{BL} & \mathbf{P}^{BR} \end{bmatrix} = \begin{bmatrix} \mathbf{D}^{L} & \mathbf{D}^{R} \end{bmatrix} \mathbf{P} \begin{bmatrix} \mathbf{D}^{T} \\ \mathbf{D}^{B} \end{bmatrix}, \quad (12)$$

which is easily done in parallel.

#### 2.5 Hull Classification

In Section 2.1 we showed how to determine whether a given ray hits the convex hull of a given patch by looking at the allocation of the projected patch control points in the ray-based projected 2D coordinate frame. While conceptually useful, we can be more efficient at implementation by making four observations.

First, we do not need to do the projections. We need only determine whether a given control point lies above or below the U or V plane. This is a simple evaluation of the plane

equation using the control point. Recall that in a 3D space, the distance of a point **P** at (x, y, z) to a plane as given in (1) is

$$d = \frac{Ax + By + Cz + E}{\sqrt{A^2 + B^2 + C^2}}.$$
 (13)

This distance is either negative or positive depending on which side of the plane the point  $\mathbf{P}$  is located. The normal defines the positive direction. The distance is zero if  $\mathbf{P}$  is on the plane.

Second, we do not need the actual distance, only need its sign to determine whether the point is above or below the plane. (We consider the infrequently occurring case where the control point is contained within the plane to be "above" for hull classification purposes.)

Since we are only interested in the sign of d, which is solely determined by the numerator of the right-hand side of (13), we can define the sign distance as

$$d_s = Ax + By + Cz + E, (14)$$

or, in matrix form,

$$d^{s} = \begin{bmatrix} A & B & C & E \end{bmatrix} \begin{bmatrix} \mathbf{P} \\ 1 \end{bmatrix}.$$
(15)

Our hull classification algorithm says that, for either U or V, if the signs of the sixteen sign distances are either all positive or all negative, the ray misses the patch. If this is untrue for both planes, the ray may hit the convex hull of the patch and hence it may also intersect the patch itself.

A third observation is that we do not necessarily need to test all 16 points. Finding two points on opposite sides of the plane would imply possible intersection, but as this requires sequential evaluation, we cannot take advantage of this observation.

Fourth, the sign calculations for both planes can also be done simultaneously. The explicit computations for calculating the 16 sign distances with respect to the planes U and V are

$$\mathbf{d}_{u}^{s} = \begin{bmatrix} A_{u} & B_{u} & C_{u} & D_{u} \end{bmatrix}$$
(16)  
$$\begin{bmatrix} P_{00x} & P_{01x} & \dots & P_{33x} \\ P_{00y} & P_{01y} & \dots & P_{33y} \\ P_{00z} & P_{01z} & \dots & P_{33z} \\ 1 & 1 & \dots & 1 \end{bmatrix}.$$

and similarly for  $\mathbf{d}_v^s$ . If all components of either  $\mathbf{d}_u^s$  or  $\mathbf{d}_v^s$  have the same sign, the ray cannot intersect the patch. Otherwise, the patch needs to be further divided into smaller subpatches for a more accurate approximation.

#### 2.6 Patch Center

As stated in Section 2.1, it is necessary to find the center of a given patch for two reasons. First, an intersection is ap-

proximated by the center of a sufficiently small patch. Second, in finding the approximated intersection points, our algorithm must sort out four subpatches (from all possibly-hit subpatches) closest to the origin of the ray. We assume here that "closeness" means the distance between the origin of the ray and the center of a given patch.

One way to define the center of the patch would be to take the arithmetic mean of the control points. This would be simple to compute, but we have no guarantee that this point actually lies on the patch, a desirable feature for the final intersection approximation.

Instead, we observe from (4) that for a given patch  $\mathbf{Q}(u, v)$ , an unambiguous "parametric center"  $\mathbf{C}$  can be defined:

$$\mathbf{C} \equiv \mathbf{Q}\left(\frac{1}{2}, \frac{1}{2}\right) = \mathbf{U}\mathbf{M}\mathbf{P}\mathbf{M}^{t}\mathbf{V}$$
(17)

where

$$\mathbf{U} = \mathbf{V}^t = \begin{bmatrix} \frac{1}{8} & \frac{1}{4} & \frac{1}{2} & 1 \end{bmatrix}.$$

Based on the above, we can derive

$$\mathbf{C} = \frac{1}{64} \begin{bmatrix} 1 & 3 & 3 & 1 \end{bmatrix} \mathbf{P} \begin{bmatrix} 1 \\ 3 \\ 3 \\ 1 \end{bmatrix}.$$
(18)

#### 2.7 Patch Distance

We define patch distance to be the distance between the origin of the ray and the center of a given patch. Given two points  $\mathbf{P}_1 = (x_1, y_1, z_1)$  and  $\mathbf{P}_2 = (x_2, y_2, z_2)$  in 3-space, the distance between them is normally defined by an  $L_2$  (Euclidean) metric:

$$d = \sqrt{(x_2 - x_1)^2 + (y_2 - y_1)^2 + (z_2 - z_1)^2}.$$
 (19)

However, to simplify the computation, we redefine the distance as an  $L_1$  (Manhattan) metric:

$$d = |x_2 - x_1| + |y_2 - y_1| + |z_2 - z_1|.$$
(20)

The distance computation from the ray origin must also include a sign. Positive signs indicate positive ray parameter (t) values. If the cosine of the angle between D and the vector from O to C (given by  $\frac{(C-O)\cdot D}{|C-O||D|}$ ) is positive, the ray points towards (i.e., may hit) the patch, otherwise it points away from (i.e., definitely misses) the patch.

Again, since we are only interested in the sign of the cosine, all we need to calculate is  $(\mathbf{C} - \mathbf{O}) \cdot \mathbf{D}$ , so we can sort patches

using the signed ray/patch distance

$$d^{p} = \begin{cases} d & \text{if } (\mathbf{C} - \mathbf{O}) \cdot \mathbf{D} \ge 0\\ -1 & \text{if } (\mathbf{C} - \mathbf{O}) \cdot \mathbf{D} < 0 \end{cases}$$
(21)

Using -1 as a flag allows us to remove that particular candidate from further processing.

#### 2.8 The Culler

In our intersection algorithm, each of four patches is divided into four subpatches, a total of 16 subpatches. Each subpatch is then evaluated to see if its convex hull intersects with the ray (see Section 2.1). From all qualifying subpatches, the  $N_i$  (at most) with the shortest ray/patch distance will be selected for further approximation. The sorting task is accomplished using Quicksort, a standard sorting algorithm withrun time efficiency on an input array of size n. This combination of sorting and selection constitutes the "Culler" module.

# 2.9 Summary: An Algorithm for Finding the Ray/ Patch Intersection

We now present an implementation of the ray/patch intersection algorithm described in Section 2.1. As mentioned above, we have taken  $N_i$  to be 4. We also use intermediate buffers to hold patches.

- 1. Subdivide the input patch into four subpatches (Section 2.4) and put them into buffer1, which can store at most four subpatches. Generate planes *U* and *V* from the ray parameters (Section 2.3).
- If the desired number of iterations is reached, go to Step
   Otherwise, subdivide each patch in buffer1 into four subpatches (Section 2.4) and store them in buffer2, which can hold at most 16 subpatches.
- Evaluate each patch in buffer2 by hull classification (Section 2.5). Calculate the centers of the patches (Section 2.6) and their distances to the origin of the ray (Section 2.7).
- 4. Sort the patches in buffer2 to find the four patches closest to (but not "behind", i.e. t 0) the ray origin (Section 2.8) and put them into buffer1. Then go back to Step 2.
- 5. Output the center of the closest of the four subpatches.

Assuming that there is an intersection point, after n iterations the resulting subpatch will be approximately 1/2n of the original (linear) patch dimensions. As long as the input patch is

Function	Designation	Description
PlaneGenerator	M0	generates the two planes $U$ and $V$ from a given ray
PatchSubdivider	M1	divides an input patch into four subpatches
HullClassifier	M2	classify the hull
PatchCenter	M2	compute the center of a patch
PatchDistance	M2	compute the distance from the patch center to the ray origin
Culler	Culler	select the four subpatches nearest to the ray origin from sixteen candidates

Table 1: Modular Decomposition. The designations refer to Figure 6.



Figure 5: Mean Running Times of Functional Building Blocks (in msec.)

reasonably small, the four final subpatches will converge to a tiny area, the center of the closest of which will be considered the intersection point.

## **3** Test Implementation

All the functional blocks required by the proposed ray/patch intersection algorithm are listed in Table 1. After coding them as C functions, we obtained the execution times shown in Figure 5. These were measured on a PC Workstation with a 350MHz Intel Pentium II-MMX processor using the Windows<sup>T</sup> M NT 4.0 operating system with networking activities disabled and the Visual C++ compiler version 6.0 with the Maximize Speed option on.

These data reveal the load distribution among the different computing tasks and serve as guidelines for balancing the pipeline stages and determining the hardware/software partition.

In order to verify our algorithm, we developed a user interface in which the user can specify arbitrary rays and patches. Using the standard "teapot" patches as the benchmark, we also compared the results obtained from our algorithm with those from Nishita's algorithm [9] (based on the Bézier clipping technique). We found that the intersection points generated by the two algorithms are within the accuracy specified for the Bézier clipping approach.

## 4 System Architecture

The overall system architecture for physical implementation of our algorithm is shown in Figure 6. Note that the block M2 contains three modules listed in Table 1: HullClassifier, PatchCenter, and PatchDistance.

The proposed architecture contains eleven pipelined stages. Stage 0 executes Step 1 of the algorithm; Stages 1 to 10 execute the loop body of the algorithm. They are identical except that stage 10 outputs centers of the last four subpatches that will be taken as the intersection points. Within each stage, the inherent parallelism of the algorithm is exploited by the functional building blocks. The number of stages was determined based on observations of the algorithms convergence rate during the software emulation process. Throughput of the overall system will be determined by the latency of a single stage.

Note that this architecture illustrates the datapath stream of our algorithm with coarse-grained pipeline stages. It is open to refined intra-stage pipelining and hardware/software partitioning, based on specific performance requirements and implementation constraints determined by different applications.

## 5 Conclusion and Subsequent Work

In this study, we have presented an algorithm for solving the ray/Bézier patch intersection problem. Although its computational complexity is on the same order of some other known algorithms, the proposed algorithm has the advantage of being suitable for parallel and pipelined execution. The corresponding systems architecture for physical realization has also been developed with all the functional building blocks specified.

Work based on these results has already been completed in [10]. For performance estimation, the pipeline was refined, load-balanced and mapped into multiple digital signal processors (DSPs) and custom-designed hardware. The results show that by reducing the grain scale of the pipeline and supporting the inherent parallelism of the computing tasks within its stages, a system throughput of over  $10^{6}$  ray/patch intersections

per second is achievable.

We anticipate that with the current level of rapid technological advances, practical systems based on this study could be built in the near future.

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Figure 6: System Architecture